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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995. See The American Inventors Protection Act of 1999 (AIPA).

Application Number	10/052,779
Filing Date	January 23, 2002
Examiner Name	T. Cunningham
First Named Inventor	Atsushi Kawasumi
Group Art Unit	2816
Attorney Docket Number	005405.00004

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.
NOTE: 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53 (d) (PTO/SB/29) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and Provisional Application Practice, Interim Rule, 65 Fed. Reg. 14865 (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

1. Submission required under 37 C.F.R. § 1.114

- a. ☐ Previously submitted
- i. ☐ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on _____
(Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on _____
- iii. ☐ Other _____
- b. Enclosed
- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☐ Other _____

2. Miscellaneous

- a. ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a period of _____ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17(i) required)
- b. ☐ Other _____

3. Fees The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.

- a. ☒ The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 19-0733
- i. ☒ RCE fee required under 37 C.F.R. § 1.17(e)
- ii. ☐ Extension of time fee (37 C.F.R. §§ 1.136 and 1.17)
- iii. ☐ Other _____
- b. ☐ Check in the amount of \$ _____ enclosed
- c. ☐ Payment by credit card (Form PTO-2038 enclosed)

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

Name (Print /Type)	Joseph M. Potenza	Registration No. (Attorney/Agent)	28,175
Signature	<i>Will: J. Potenza</i>	Date	December 13, 2002

Reg. No. 34,701

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, Box RCE, Washington, DC 20231, or facsimile transmitted to the U.S. Patent and Trademark Office on:

Name (Print /Type)	
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Date	

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

12/19/02
John H

In Re Application Of:

Atsushi Kawasumi

Appln. No.: 10/052,779

Filed: January 23, 2002

For: Current Mirror Circuit And Current
Source Circuit

Group Art Unit: 2816

Examiner: T. Cunningham

Atty. Dkt. No. 005405.00004

Confirmation No.: 7218

TECHNOLOGY CENTER 2800

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The Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

AMENDMENT ACCOMPANYING REQUEST FOR CONTINUED
EXAMINATION UNDER 37 C.F.R. § 1.114

Pursuant to 37 C.F.R. § 1.114 and in response to the Final Office Action dated September 13, 2002, Applicant respectfully requests continued examination of this application. A separate Request for Continued Examination and the associated fees are filed concurrently herewith.

Prior to further examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 12-15 without prejudice or disclaimer.

Please amend claims 2, 9, 17, 19, 21, and 22 into the following form:

2. (Twice Amended) A current mirror circuit comprising:

a current source;

a first MOS transistor having a gate, a drain coupled to the gate and the current source, and a

source coupled to a first power source;

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